



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Application of:  
Sompong P. Olarig

Serial No.: 09/769,953

Filed: January 25, 2001

For: COMPUTER SYSTEM HAVING  
CONFIGURABLE CORE LOGIC CHIPSET  
FOR CONNECTION TO A FAULT-  
TOLERANT ACCELERATED GRAPHICS  
PORT BUS AND PERIPHERAL  
COMPONENT INTERCONNECT BUS

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Group Art Unit: 2114

Examiner: Le, Dieu Minh T

Atty. Docket: 200304082-1  
COMP:0195  
(P98-1950)

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37 C.F.R. 1.8

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September 1, 2004  
Date

  
Robert A. Manware

Sir:

**RESPONSE AND PRELIMINARY AMENDMENT**

In response to the Final Official Action mailed on June 1, 2004, Applicant has chosen to file this Response and Preliminary Amendment, along with a Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 (filed herewith) to continue prosecution of the above-identified application. Applicant respectfully requests reconsideration of the above-identified application in view of the remarks and amendments set forth below.